**System Settings**

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| **Environment Settings** | | | | |
| **Environment Variable** | **xst** | **ngdbuild** | **map** | **par** |
| PATHEXT | .COM;  .EXE;  .BAT;  .CMD;  .VBS;  .VBE;  .JS;  .JSE;  .WSF;  .WSH;  .MSC | < data not available > | < data not available > | < data not available > |
| Path | C:\Xilinx\14.7\ISE\_DS\ISE\\lib\nt64;  C:\Xilinx\14.7\ISE\_DS\ISE\\bin\nt64;  C:\Xilinx\14.7\ISE\_DS\ISE\bin\nt64;  C:\Xilinx\14.7\ISE\_DS\ISE\lib\nt64;  C:\Xilinx\14.7\ISE\_DS\ISE\..\..\..\DocNav;  C:\Xilinx\14.7\ISE\_DS\PlanAhead\bin;  C:\Xilinx\14.7\ISE\_DS\EDK\bin\nt64;  C:\Xilinx\14.7\ISE\_DS\EDK\lib\nt64;  C:\Xilinx\14.7\ISE\_DS\EDK\gnu\microblaze\nt\bin;  C:\Xilinx\14.7\ISE\_DS\EDK\gnu\powerpc-eabi\nt\bin;  C:\Xilinx\14.7\ISE\_DS\EDK\gnuwin\bin;  C:\Xilinx\14.7\ISE\_DS\EDK\gnu\arm\nt\bin;  C:\Xilinx\14.7\ISE\_DS\EDK\gnu\microblaze\linux\_toolchain\nt64\_be\bin;  C:\Xilinx\14.7\ISE\_DS\EDK\gnu\microblaze\linux\_toolchain\nt64\_le\bin;  C:\Xilinx\14.7\ISE\_DS\common\bin\nt64;  C:\Xilinx\14.7\ISE\_DS\common\lib\nt64;  C:\Program Files\Haskell\bin;  C:\Program Files\Haskell Platform\8.0.2-a\lib\extralibs\bin;  C:\Program Files\Haskell Platform\8.0.2-a\bin;  C:\ProgramData\Oracle\Java\javapath;  C:\Windows\system32;  C:\Windows;  C:\Windows\System32\Wbem;  C:\Windows\System32\WindowsPowerShell\v1.0\;  C:\Program Files (x86)\NVIDIA Corporation\PhysX\Common;  C:\Program Files\Git\cmd;  C:\Program Files (x86)\GtkSharp\2.12\bin;  C:\Program Files\Haskell Platform\8.0.2-a\mingw\bin;  C:\Program Files\Microsoft SQL Server\130\Tools\Binn\;  C:\Program Files\dotnet\;  C:\Program Files\nodejs\;  C:\Program Files\Java\jdk1.8.0\_131\bin;  C:\Users\Jayant\AppData\Roaming\cabal\bin;  C:\Users\Jayant\AppData\Roaming\local\bin;  C:\Users\Jayant\AppData\Local\Microsoft\WindowsApps;  C:\Users\Jayant\AppData\Local\atom\bin;  C:\Program Files\Microsoft VS Code\bin;  C:\Users\Jayant\AppData\Roaming\npm | < data not available > | < data not available > | < data not available > |
| XILINX | C:\Xilinx\14.7\ISE\_DS\ISE\ | < data not available > | < data not available > | < data not available > |
| XILINX\_DSP | C:\Xilinx\14.7\ISE\_DS\ISE | < data not available > | < data not available > | < data not available > |
| XILINX\_EDK | C:\Xilinx\14.7\ISE\_DS\EDK | < data not available > | < data not available > | < data not available > |
| XILINX\_PLANAHEAD | C:\Xilinx\14.7\ISE\_DS\PlanAhead | < data not available > | < data not available > | < data not available > |

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| **Synthesis Property Settings** | | | |
| **Switch Name** | **Property Name** | **Value** | **Default Value** |
| -ifn |  | T\_flipflop.prj |  |
| -ifmt |  | mixed | MIXED |
| -ofn |  | T\_flipflop |  |
| -ofmt |  | NGC | NGC |
| -p |  | xc3s500e-4-ft256 |  |
| -top |  | T\_flipflop |  |
| -opt\_mode | Optimization Goal | Speed | SPEED |
| -opt\_level | Optimization Effort | 1 | 1 |
| -iuc | Use synthesis Constraints File | NO | NO |
| -keep\_hierarchy | Keep Hierarchy | No | NO |
| -netlist\_hierarchy | Netlist Hierarchy | As\_Optimized | as\_optimized |
| -rtlview | Generate RTL Schematic | Yes | NO |
| -glob\_opt | Global Optimization Goal | AllClockNets | ALLCLOCKNETS |
| -read\_cores | Read Cores | YES | YES |
| -write\_timing\_constraints | Write Timing Constraints | NO | NO |
| -cross\_clock\_analysis | Cross Clock Analysis | NO | NO |
| -bus\_delimiter | Bus Delimiter | <> | <> |
| -slice\_utilization\_ratio | Slice Utilization Ratio | 100 | 100% |
| -bram\_utilization\_ratio | BRAM Utilization Ratio | 100 | 100% |
| -verilog2001 | Verilog 2001 | YES | YES |
| -fsm\_extract |  | YES | YES |
| -fsm\_encoding |  | Auto | AUTO |
| -safe\_implementation |  | No | NO |
| -fsm\_style |  | LUT | LUT |
| -ram\_extract |  | Yes | YES |
| -ram\_style |  | Auto | AUTO |
| -rom\_extract |  | Yes | YES |
| -shreg\_extract |  | YES | YES |
| -rom\_style |  | Auto | AUTO |
| -auto\_bram\_packing |  | NO | NO |
| -resource\_sharing |  | YES | YES |
| -async\_to\_sync |  | NO | NO |
| -mult\_style |  | Auto | AUTO |
| -iobuf |  | YES | YES |
| -max\_fanout |  | 100000 | 500 |
| -bufg |  | 24 | 24 |
| -register\_duplication |  | YES | YES |
| -register\_balancing |  | No | NO |
| -optimize\_primitives |  | NO | NO |
| -use\_clock\_enable |  | Yes | YES |
| -use\_sync\_set |  | Yes | YES |
| -use\_sync\_reset |  | Yes | YES |
| -iob |  | Auto | AUTO |
| -equivalent\_register\_removal |  | YES | YES |
| -slice\_utilization\_ratio\_maxmargin |  | 5 | 0% |

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| **Operating System Information** | | | | |
| **Operating System Information** | **xst** | **ngdbuild** | **map** | **par** |
| CPU Architecture/Speed | Intel(R) Core(TM) i7-6800K CPU @ 3.40GHz/3400 MHz | <  data not available  > | <  data not available  > | <  data not available  > |
| Host | DESKTOP-B0TE9C2 | <  data not available  > | <  data not available  > | <  data not available  > |
| OS Name | Microsoft , 64-bit | <  data not available  > | <  data not available  > | <  data not available  > |
| OS Release | major release (build 9200) | <  data not available  > | <  data not available  > | <  data not available  > |